



विद्युत अभियंत्रण विभाग
मोतीलाल नेहरू राष्ट्रीय प्रौद्योगिकी संस्थान
इलाहाबाद – 211004 (भारत)

Department of Electrical Engineering
MOTILAL NEHRU NATIONAL INSTITUTE OF TECHNOLOGY ALLAHABAD
Allahabad – 211 004 (India)

Advertisement No- 01/2017 /Project Staff

Dated- 12 /04/2017

Advertisement for the Post of JRF/SRF/RA (On Contract)

Applications are invited from Indian nationals for the post of JRF/SRF/RA (On contract) in research project entitled “**Implementation of fast charging/discharging PHEVs integrated with modern UPS for peak demand reduction in distributed system**” sanction order no. 22(0703)/15/EMR-II dated 23/11/2015 sponsored by Council of Scientific & Industrial Research (CSIR). The duly completed application on prescribed format along with copies of supporting documents must reach to:

Office of the Dean (Research & Consultancy), Motilal Nehru National Institute of Technology, Allahabad-211004 on or before 02/05/2017. The envelop should be super scribed as “Application for the post of JRF/SRF/RA (On contract) in the project Implementation of fast charging/discharging PHEVs integrated with modern UPS for peak demand reduction in distributed system”. The position is purely temporary and will be governed by the funding agency rules & service conditions of Office of the Dean (Research & Consultancy), MNNIT Allahabad.

Number of Position : 1 (One)

Essential Qualifications : **For RA-** Possessing Doctorate (Ph.D.) or equivalent in relevant area of ELECTRICAL ENGINEERING OR having 3 years of research, teaching and design & development experience in related area after M.E/M.Tech. in ELECTRICAL ENGINEERING or equivalent.

OR

For SRF- M.E/M.Tech. in ELECTRICAL ENGINEERING or equivalent.

OR

For JRF- Possessing B.E/B.Tech. in ELECTRICAL ENGINEERING or equivalent with 55% marks.

Fellowship : **For RA** - Rs. 36,000/- pm + HRA (as per rules)

For SRF- Rs. 14,000/- pm + HRA (as per rules)

For JRF- Rs. 12,000/- pm + HRA (as per rules).

Other Benefits : Accommodation may be available as per the availability and the Institute norms in lieu of HRA. Candidates having CPI 6.5 or 60% marks in M. Tech may be eligible to register for Ph.D. program of the Institute. Facility of yearly leave, carryover leave, medical benefits etc. may be available as per applicable rules of the project staff.

Age Limit : **35 years** for RA or 32 years for SRF or 28 years for JRF on the last date of application (The upper age limit is relaxable up to 5 years in the case of candidates belonging to SC/ST/OBC/PH and women candidates)

Tenure of Appointment : Appointment will be made on contract basis for a period of three year initially, which may be extended depending on the performance evaluation on yearly basis till the end of project.

- Note:**
1. The applicant will be responsible for the authenticity of information, other documents and photographs submitted.
 2. The Institute reserves the right to accept application at any time, and consider candidates of exceptional credentials without applications. Qualification and experience may be relaxed by the Institute at any point of time for otherwise exceptional candidates.
 3. Mere, possessing the prescribed qualification does not ensure that the candidate would be called for Interview. The Candidates will be shortlisted on the basis of merit and need of the project.
 4. Shortlisted Candidates will be informed by e-mail about the interview date. So, the candidate must provide valid E mail IDs in their applications.
 5. Shortlisted candidates have to present themselves for the interview on the interview date with updated CV and original and attested photocopies of mark sheets/ certificates in support of their academic qualifications.
 6. Applicants in employment (government or any other organization) are required to submit a “**No Objection Certificate**” from the employer at the time of interview.
 7. No TA/DA will be paid for appearing in the interview.

Name of the PI : Dr. Asheesh Kumar Singh

Designation : Associate Professor,

Department : Electrical Engineering Department